







FIG. 3

DRIVER MODE	DRIVER STATE	SLEW CURRENT	HIGH-SIDE TRANSISTORS ON	LOW-SIDE TRANSISTORS ON
Dynamic	Low (ACTIVE)	Dynamic + Standby	Q75, Q27	Q42A, Q42B
Dynamic	HIGH (ACTIVE)	Dynamic + Standby	Q85, Q41	Q74, Q28
Dynamic	INHIBIT	Dynamic + Standby	Q29, Q27	Q31, Q28
Termination	Low (ACTIVE)	Standby	Q75, Q27	Q42A, Q42B
Termination	HIGH (ACTIVE)	Standby	Q85, Q41	Q74, Q28
Termination	INHIBIT	Standby	Q29, Q27	Q31, Q28

FIG. 4